

Docket No.: 9485.006.20-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Milton Feng et al.

Application No.: Not Yet Assigned

Customer No.: 30827

Filed: March 3, 2004

Art Unit: N/A

For: INTERGRATED PHOTODETECTOR AND
HETEROJUNCTION BIPOLAR
TRANSISTORS

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new continuation patent application submitted herewith. The listed references have been previously cited by the Examiner in related case Serial No. 10/379,862 filed March 4, 2003.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0911, under Order No. 9485.006.20-US. A duplicate copy of this paper is enclosed.

Dated: March 3, 2004

Respectfully submitted,

By 

Song K. Jung

Registration No.: 35,210

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Washington, DC 20006

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Attorney for Applicant

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	Not Yet Assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Filing Date	March 3, 2004
				First Named Inventor	Milton Feng
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	9485.006.20-US
Sheet	1	of	2		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-4,377,723	03-22-1983	DALAL	
	AB	US-5,448,080	09-05-1995	HAN et al.	
	AC	US-5,342,453	08-30-1994	OLSON	
	AD	US-5,453,135	09-26-1995	NAKAGAWA et al.	
	AE	US-5,557,117	09-17-1996	MATSUOKA et al.	
	AF	US-5,682,046	10-28-1997	TAKAHASHI et al.	
	AG	US-5,684,308	11-04-1997	LOVEJOY et al.	
	AH	US-5,689,122	11-18-1997	CHANDRASEKHAR	
	AI	US-5,978,401	11-02-1999	MORGAN	
	AJ	US-6,055,262	04-25-2000	COX et al.	
	AK	US-6,107,151	08-22-2000	ENQUIST	
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	AS	US-6,429,463	08-06-2002	MAUK	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	BA					
	BB					

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	D. Caruth et al., Abstract, "A 40 Gb/s Integrated Differential PIN+TIA with DC Offset Control Using InP SHBT Technology, Xindium Technologies, Inc., et al., 3 pp. presented October 21, 2002	
	CB	P.A. Houston, "High Frequency Heterojunction Bipolar Transistor Devise Design and Technology," Electronics & Communication Engineering Journal, October 2000, pp. 220-228	

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Examiner Signature		Date Considered	
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	AE	US			
	AF	US			
	AG	US			
	AH	US			

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	CA	Sawdai et al., "Self-Aligned Technology For GaAs and InP-based Heterojunction Bipolar Transistors (HBTs), U.S. Army Research Office DAAL03-92-G-0109, NEC Inc. and Bell Northern Research, www.eecs.umich.edu/dp-group/research/selfalignedHBTS.html , 1999			
	CB	Alekseev et al., "High-Isolation W-band InP-based PIN Diode Monolithic Integrated Switches," 1997 IEEE/Cornell Conference on Advanced Concepts in High-Speed Semiconductor Devices and Circuits, August 4-6, 1997			
	CC	Stephen A. Campbell, "The Science and Engineering of Microelectronic Fabrication (Second Edition)," February 1, 2001, Chapter 18, pp. 465-480			

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